

Fig. 1

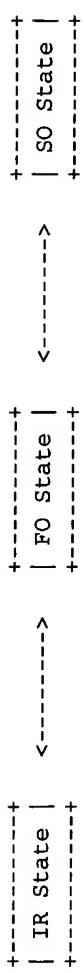


Fig. 2

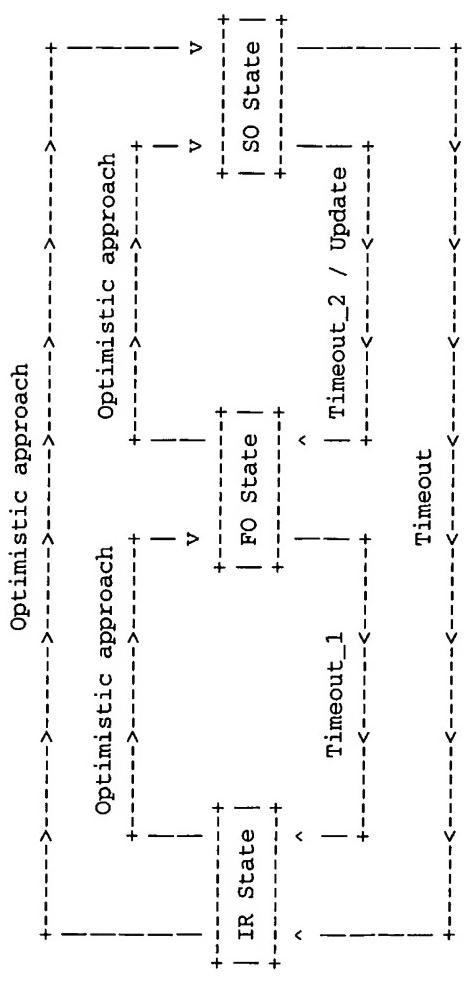


Fig. 3

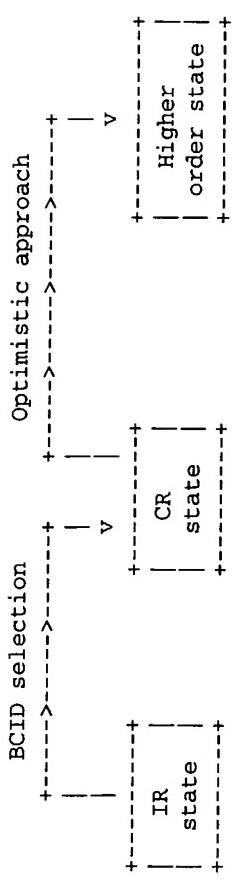


Fig. 4

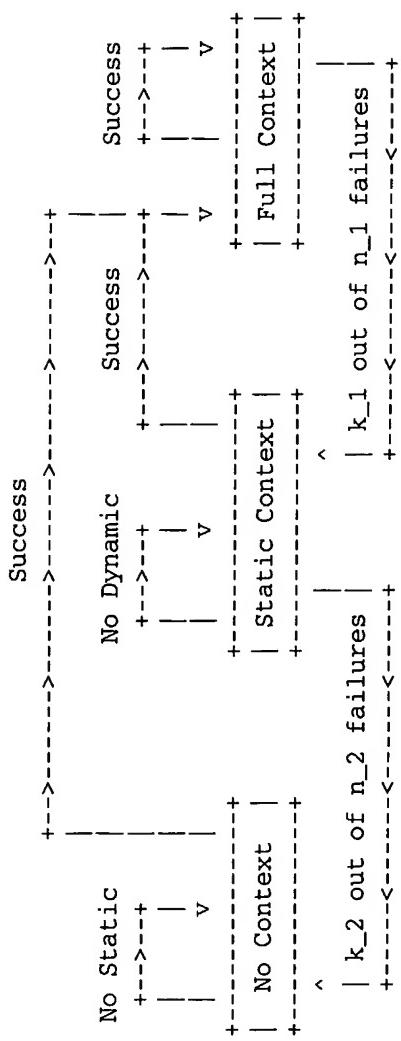
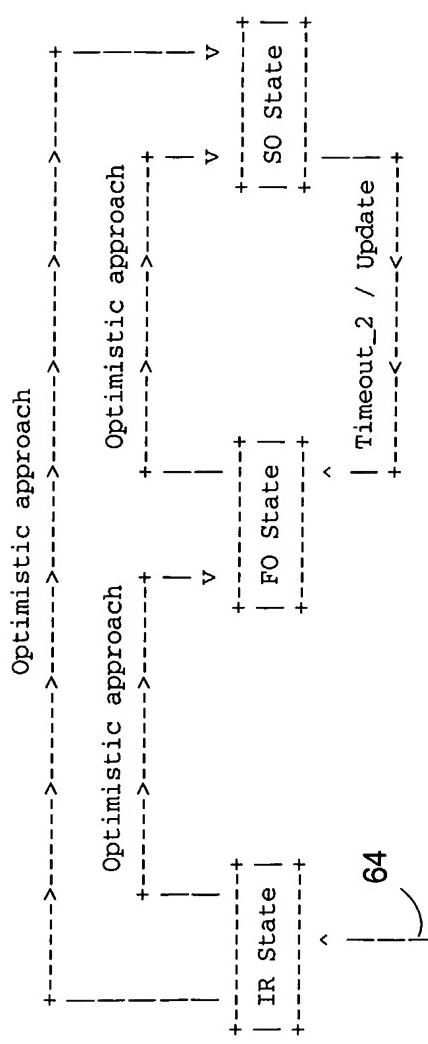


Fig. 6



State Transition Trigger
(External signal from underlying system)

Fig. 5

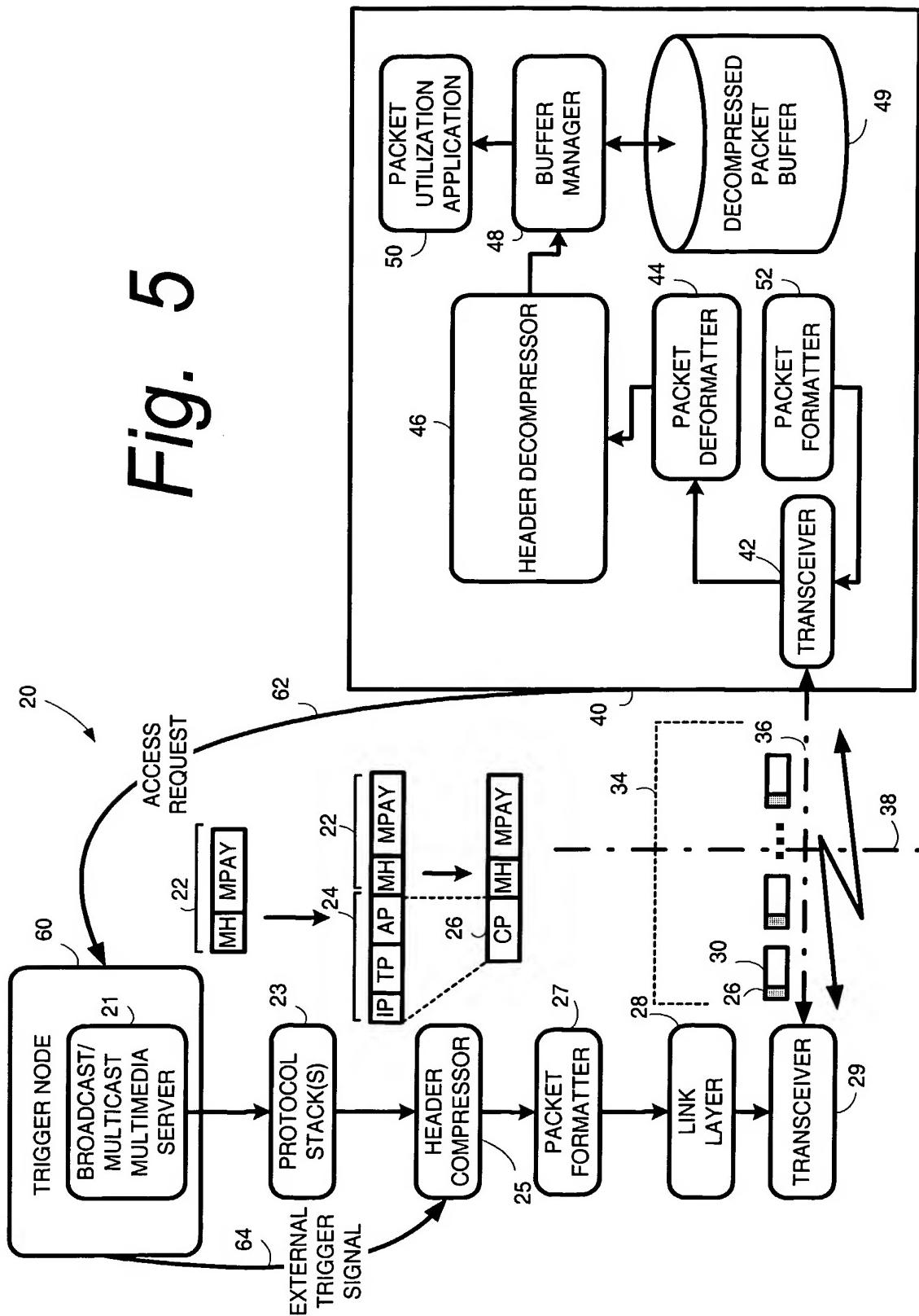


Fig. 7A

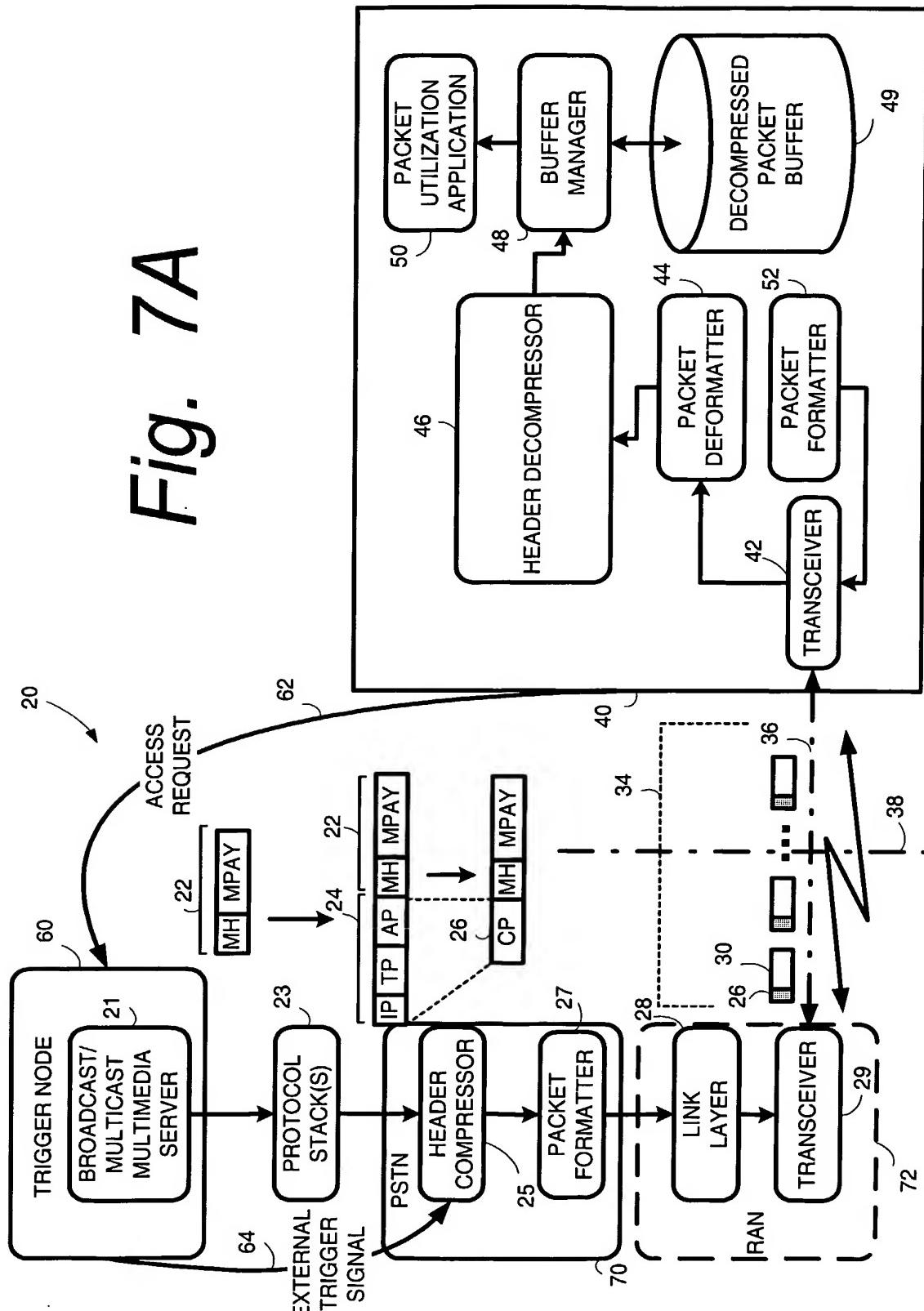
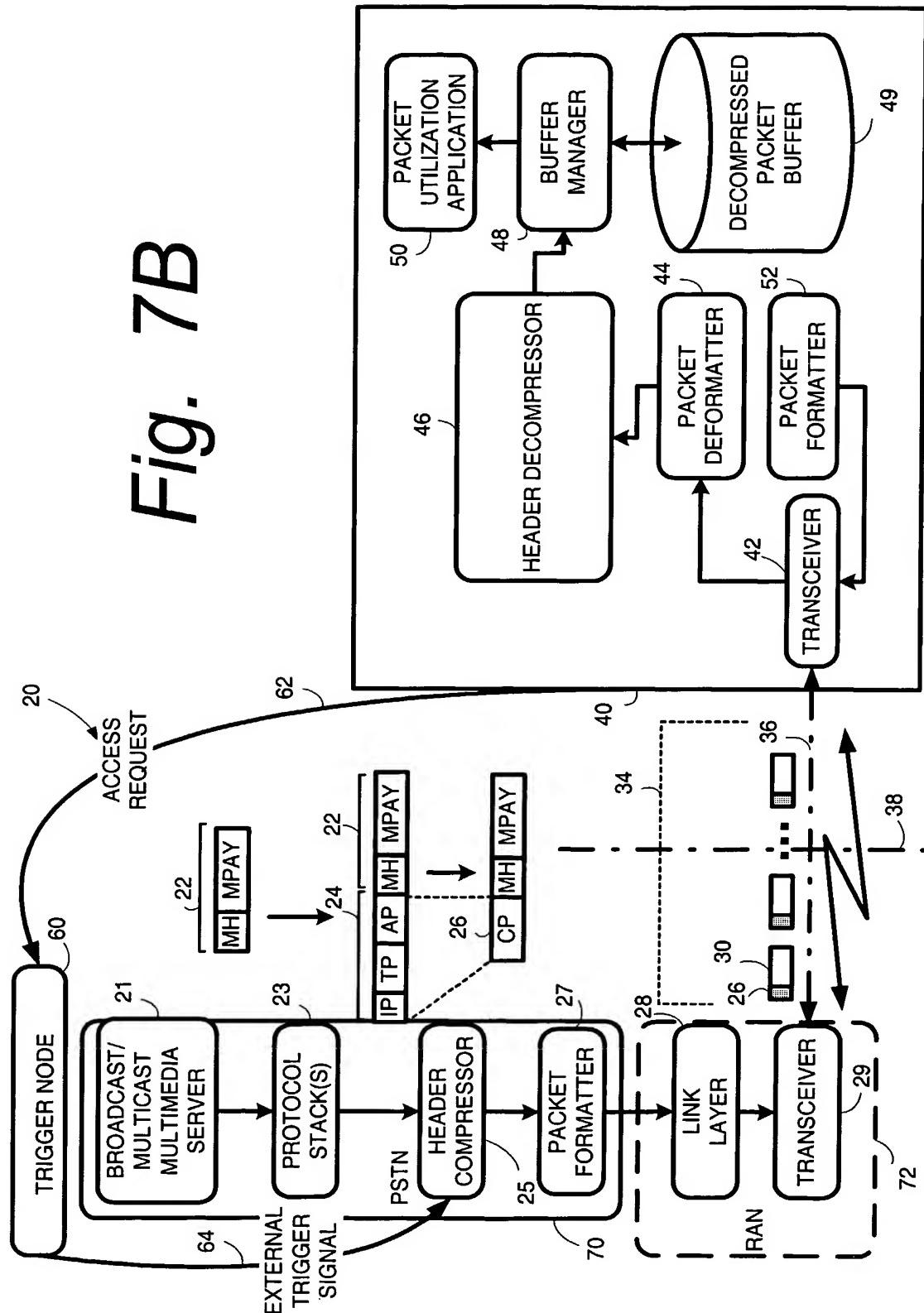


Fig. 7B



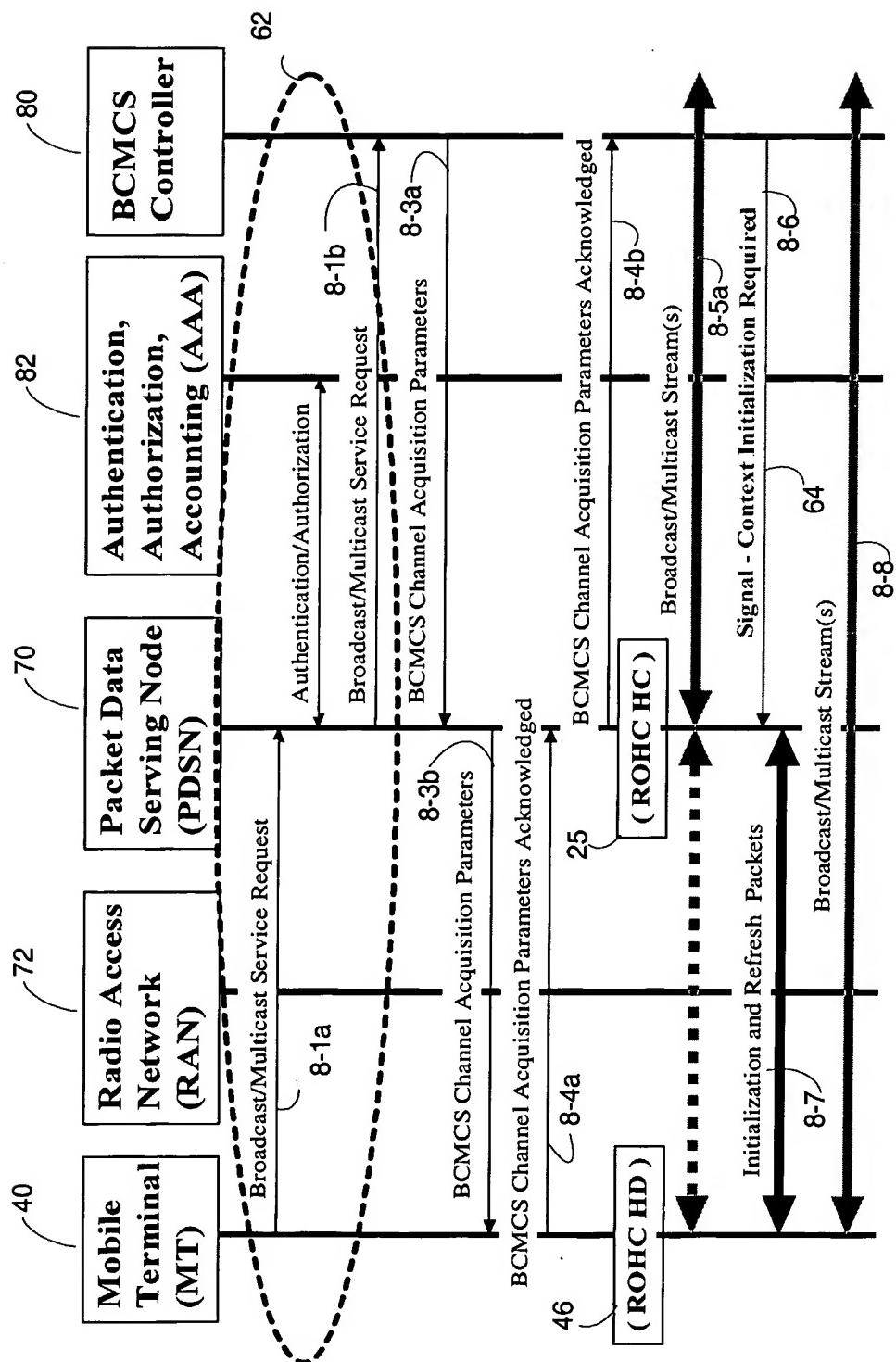
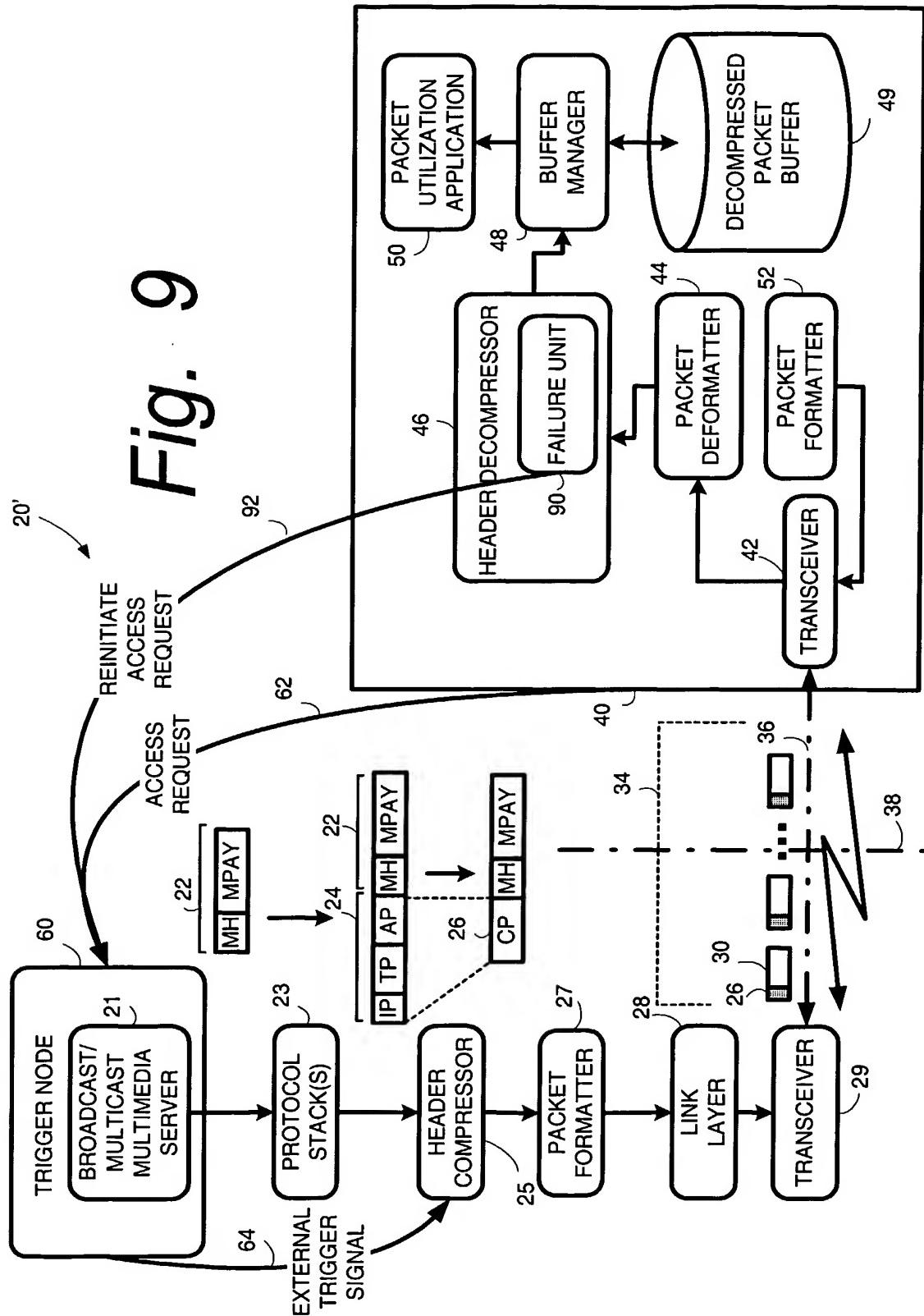


Fig. 8

Fig. 9



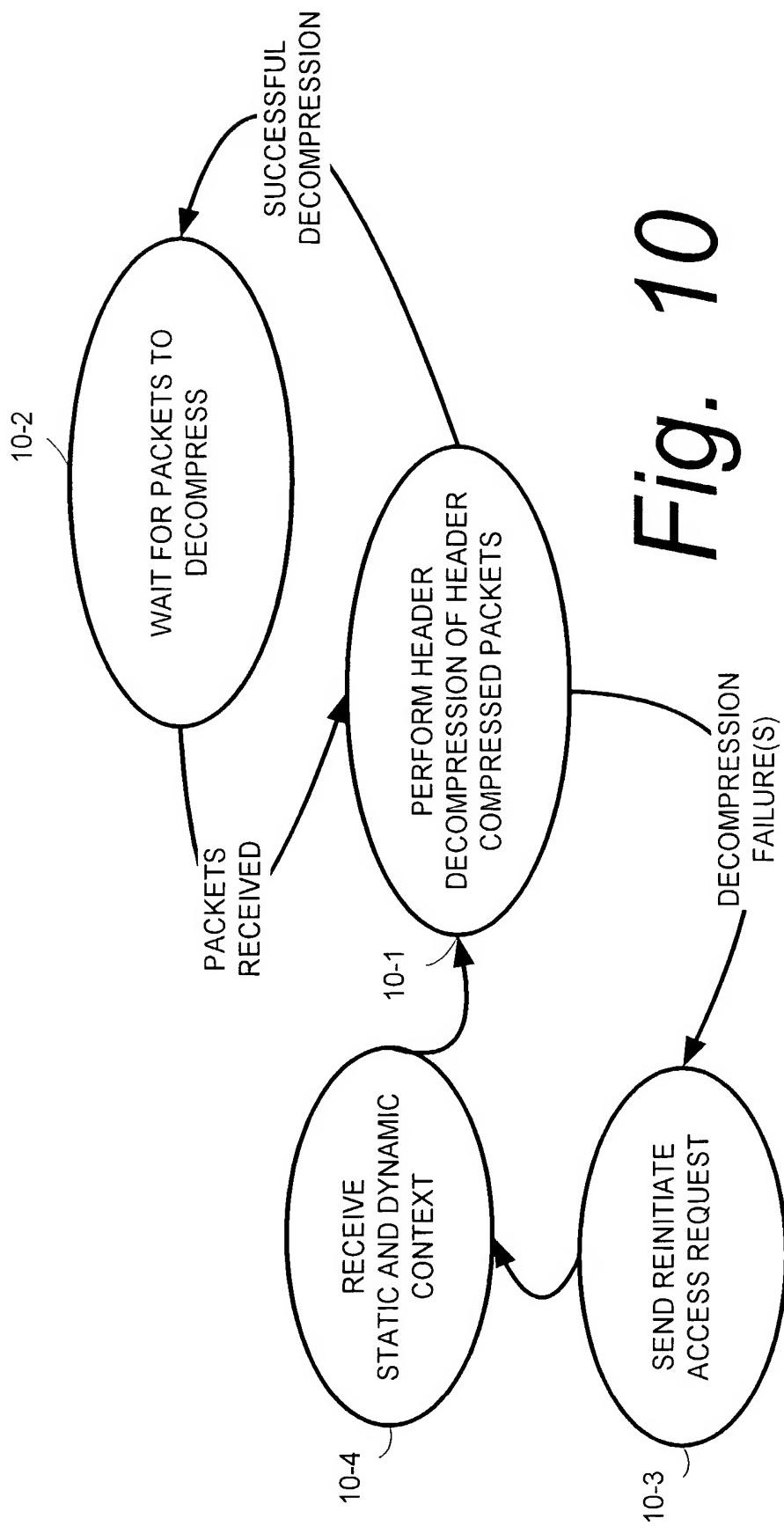


Fig. 10